



WINSTAR Display Co.,Ltd.
華凌光電股份有限公司



SPECIFICATION

MODEL NO. : WL0F00035000XGAACSA00

Summary

3.5 Inch Smart Display Feature

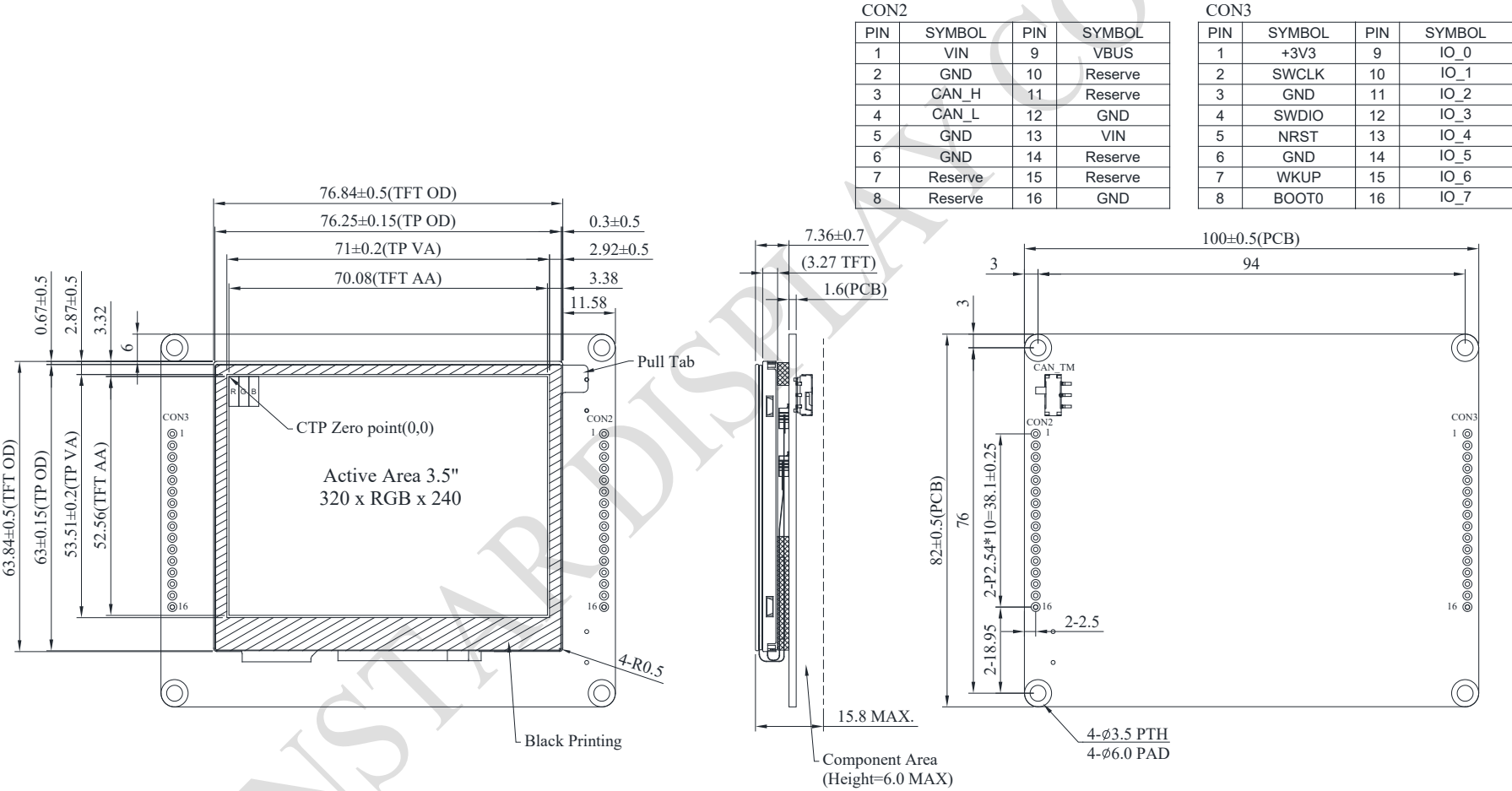
1. Input voltage dynamic range 5V to 28V, typical voltage 12V.
2. Self-testing after booting function.
3. CAN bus communication interface.
4. Supports Custom CAN ID protocol, default baud rate at 250Kbps.
5. Built in 16MB external flash memory to store the font and Object Dictionary Data.
6. Support capacitive touch panel (CTP).
7. Build in buzzer and can be controlled by Master Device.
8. Demo set HOST can be used on multiple platforms, such as Computer (with USB to CAN Dongle), MCU, Raspberry Pi (with PiCAN2).
9. GPIO PIN support
10. Polarity diode protection in power root.

Product information

General information

Item	Standard Value	Unit
Operating voltage	5 ~ 28	Vdc
Communication Interface	CAN bus	--
MCU	STM32F750	N/A
Flash ROM	16	MB
SDRAM	8	MB
LCD display size	3.5	inch
Dot Matrix	320× 3(RGB) × 240	dot
Module dimension	100(W) x 82(H) x 13.2(D)	mm
Active area	70.08(W) x 52.56(H)	mm
Pixel pitch	0.219 x 0.219	mm
Brightness	Min: 300; Typ: 400	cd/m ²
LCD type	TFT, Normally Black, Transmissive	
View Direction	80/80/80/80	
Aspect Ratio	4:3	
Touch Panel	Projected Capacitive Touch Panel	
Surface	Glare	

Contour Drawing



The non-specified tolerance of dimension is $\pm 0.3\text{mm}$.

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-30	—	+80	°C
Storage Temperature	TST	-30	—	+80	°C

Electrical Characteristics

Item	Symbol	Min	Typ	Max	Unit
Supply Voltage	VIN	5	12	28	V
Supply Current	ICC	-	150	-	mA
I/O pin Voltage	--	--	--	3.3	V

BOM

Item	Description
LCM	WF35XTWACDNG0#
PCBA	PB1V0003R500XA461X00

Interface

CON2 definition:

Pin	Symbol	Function	Remark
1	VIN	Power +5V~28V	Input
2	GND	Power GND	--
3	CAN_H	CAN bus D+	I/O
4	CAN_L	CAN bus D-	I/O
5-16	Reserved	--	--

CON3 definition:

Pin	Symbol	Function	Remark
1	+3V3	3.3V power for JTAG interface	Output
2	SWCLK	CLK pin for JTAG interface	Input
3	GND	GND for JTAG interface	--
4	SWDIO	Data pin for JTAG interface	I/O
5	NRST	Reset pin for JTAG interface	Input
6	GND	Power GND	--
7	WKUP	WKup,ADC,Timer,Event,I/O	Input
8	BOOT0	Lo: Internal flash boot (Default) Hi: External flash boot	Input
9	IO_0	ADC,DAC,Timer,Event,I/O @3.3V	I/O
10	IO_1	ADC,Timer,Event,I/O @3.3V	I/O
11	IO_2	ADC,Timer,Event,I/O @3.3V	I/O
12	IO_3	RST,Timer,Event,I/O @3.3V	I/O
13	IO_4	RST,Timer,Event,I/O @3.3V	I/O
14	IO_5	ADC,Timer,Event,I/O @3.3V	I/O
15	IO_6	RST,Timer,Event,I/O @3.3V	I/O
16	IO_7	RST,ADC,Event,I/O @3.3V	I/O

@3.3V : I/O allow below 3.3V input, If exceeding 3.3V may cause MCU damage.