



WINSTAR Display Co.,Ltd.
華凌光電股份有限公司



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SPECIFICATION

MODULE NO.:

WO1602L-TFH

General Specification

Item	Dimension	Unit
Number of Characters	16 characters x 2 Lines	—
Module dimension	62.8 x 23 x 6.3	mm
View area	51.5 x 12.23	mm
Active area	47.6 x 9.74	mm
Dot size	0.48 x 0.54	mm
Dot pitch	0.53 x 0.59	mm
Character size	2.60 x 4.67	mm
Character pitch	3.00 x 5.07	mm
Duty	1/16 , 1/5 Bias	
Backlight Type	LED	
Controller IC	ST7032Ai	
Interface	I2C	

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	°C
Storage Temperature	T_{ST}	-30	—	+80	°C
Input Voltage	V_{IN}	-0.3	—	$V_{DD}+0.3$	V
Power Supply Voltage	$V_{DD}-V_{SS}$	-0.3	—	+4.0	V
LCD Driver Voltage	V_{LCD}	-0.3	—	7.0	V

Electrical Characteristics

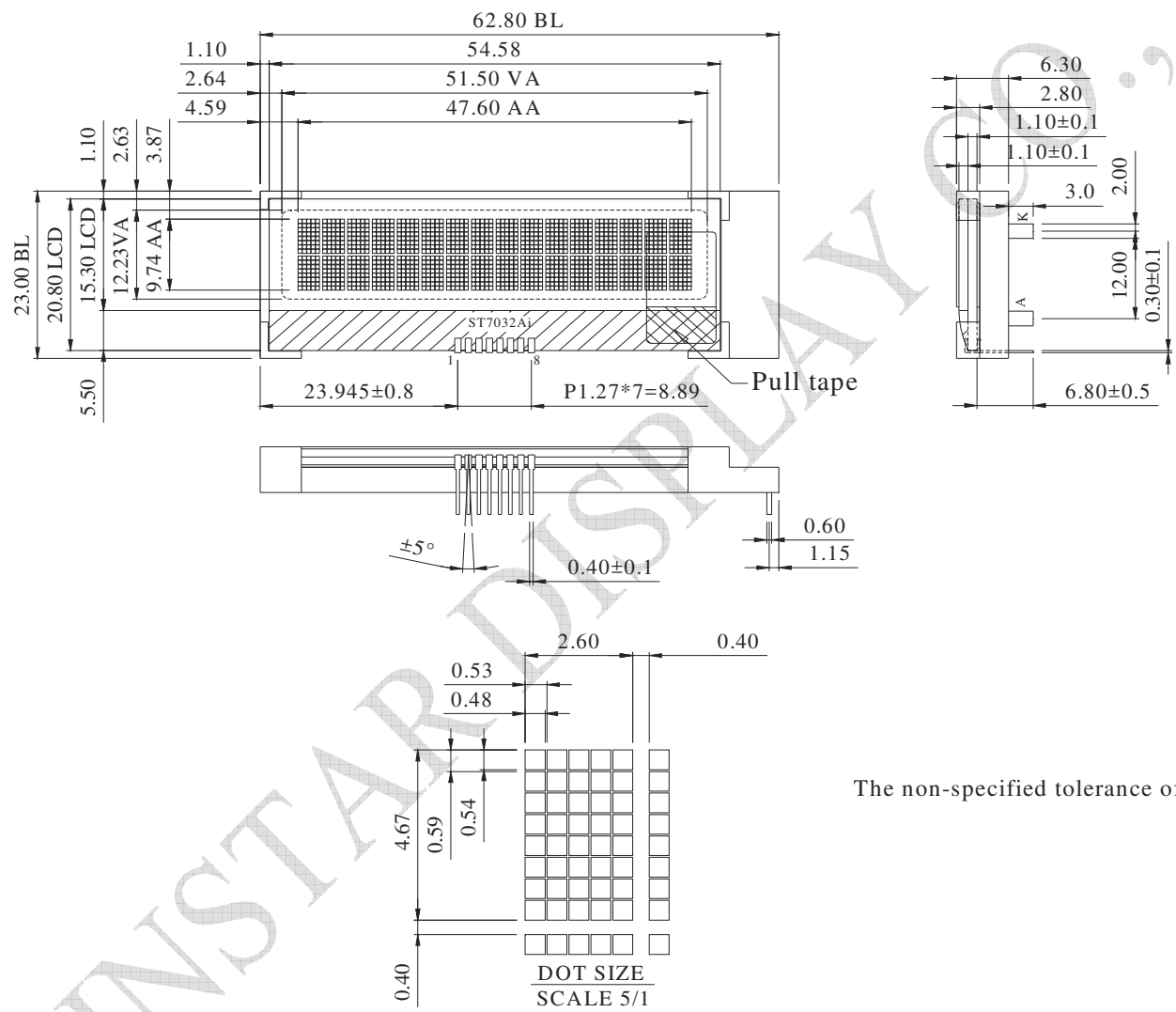
Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	$V_{DD}-V_{SS}$	—	2.9	3.0	3.1	V
Supply Voltage For LCD	V_{OP}	$T_a=-20^{\circ}\text{C}$	—	—	—	V
		$T_a=25^{\circ}\text{C}$	4.3	4.5	4.7	V
		$T_a=70^{\circ}\text{C}$	—	—	—	V
Input High Volt.	V_{IH}	—	$0.7 V_{DD}$	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	V_{SS}	—	$0.3V_{DD}$	V
Supply Current(No include LED Backlight)	I_{DD}	—	—	0.2	0.5	mA

Interface Pin Function

Pin No.	Symbol	Description
1	VG	VG is the LCD driving voltage for segment circuits. VGO is the output of VG regulator. VGS is the feedback of VG regulator. VGI is the VG input of segment circuits. $1.6 \leq VG < VDD2$.
2	XV0	XV0 is the LCD driving voltage for common circuits at positive frame. XV0OUT is the output of XV0 regulator. XV0S is the feedback of XV0 regulator. XV0I is the V0 input of common circuits.
3	V0	V0 is the LCD driving voltage for common circuits at negative frame. V0OUT is the output of V0 regulator. V0S is the feedback of V0 regulator. V0I is the V0 input of common circuits. Be sure that: $V0 \geq VG > VM > VSS \geq XV0$ (under operation).
4	VDD	Power supply input.
5	VSS	Ground
6	SDA	Serial data input
7	SCL	Serial clock input
8	RST	Hardware reset input pin. When RSTB is "L", internal initialization is executed and the internal registers will be initialized

Contour Drawing & Block Diagram

PIN NO.	SYMBOL
1	VG
2	XV0
3	V0
4	VDD
5	VSS
6	SDA
7	SCL
8	RST



The non-specified tolerance of dimension is ±0.3 mm .